What is claimed is:

1. A method of driving a liquid crystal panel having pixels arranged at each intersection between gate lines and data lines in a matrix type in an inversion system, comprising the steps of:

setting at least one pixel block each of which includes at least two data lines within the liquid crystal panel;

allowing the adjacent pixels in a gate line direction within the pixel block to respond to data signals having the same polarity; and

allowing the pixels within the other pixel areas except for the pixel block to respond to data signals having a polarity contrary to the adjacent pixels at the left and right sides thereof.

2. The method as claimed in claim 1, wherein the pixel block is positioned at a boundary portion between column drivers.

3. The method as claimed in claim 1, wherein the pixel block includes at least two data lines to which a data is applied from the same column driver.

4. The method as claimed in claim 1, wherein all the pixels within the liquid crystal panel responds to the data signals having a polarity inverted every frame.

5. An apparatus for driving a liquid crystal panel having pixels arranged at each intersection between gate lines

and data lines in a matrix type in an inversion system, comprising:

first signal supplying means for setting at least one pixel block each of which includes at least two data lines within the liquid crystal panel to apply data signals having the same polarity to the adjacent pixels in a gate line direction within the pixel block; and

second signal supplying means for applying data signals having a polarity contrary to the adjacent pixels at the left and right sides thereof to the pixels within the other pixel areas except for the pixel block area.

6. The apparatus as claimed in claim 5, further comprising:

line-inversion control means for controlling the first signal supplying means to apply the data signals having the same polarity to the adjacent pixels in the gate line direction; and

dot-inversion control means for controlling the second signal supplying means to apply the data signals having a polarity contrary to the adjacent pixels at the left and right sides thereof.

7. The apparatus as claimed in claim 5, wherein the first and second signal supplying means comprises:

at least two signal inverters for responding to control signals applied from the line-inversion control means and the dot-inversion control means to invert phases of input data signals.

8. The apparatus as claimed in claim 7, wherein all of the

odd-numbered signal inverters supplied with odd-numbered data signals and the even-numbered signal inverters supplied with even-numbered data signals respond to the control signal from the line-inversion control means to invert the input data signals.

9. The apparatus as claimed in claim 7, wherein any one of the odd-numbered signal inverters supplied with odd-numbered data signals and the even-numbered signal inverters supplied with even-numbered data signals respond to the control signal from the line-inversion control means to invert the input data signals.